



**SYLLABUS**  
**NT209 - COMPUTER SYSTEM PROGRAMMING**

**1. GENERAL INFORMATION**

Course name (Vietnamese):	<b>Lập trình hệ thống</b>
Course name (English):	<b>Computer System programming</b>
Code:	NT209
Type of course:	Fundamental
Department:	Faculty of Computer Networks and Communications
Instructor:	PhD. Pham Van Hau, MSc. Do Thi Huong Lan, MSc. Do Thi Thu Hien Email: hiendtt@uit.edu.vn
Number of credits:	3
Theory:	2
Lab:	1
Self-study:	
Prerequisite course(s):	
Pre-course(s):	IT001 - Introduction to programming IT006 - Computer architecture

**2. COURSE DESCRIPTION**

This course aims to provide students with fundamental skills and knowledge of:

- Basic concepts of computer-level programming in assembly language: data type, registers, memory, instructions... to understand how a program in a high-level programming language can be presented at the computer level and vice versa.
- Basic knowledge and skills in program optimization, concepts of stack, pointer, cache, and other computer system elements... to offer students the ability to build programs with better performance.
- Basic knowledge and skills in reverse engineering, buffer overflow attack, and program debugging.

### 3. COURSE GOALS

Table 1.

Goal No.	Goal description	Program outcomes	Level (Bloom)
<i>G1</i>	Describe the functions of components and data presentation in a computer system	<i>L02 (2.1, 2.2)</i>	<i>Knowledge - 3</i>
<i>G2</i>	Describe the functions of assembly instruction in the operation of programs	<i>L02 (2.1, 2.2, 2.4)</i>	<i>Knowledge - 3</i>
<i>G3</i>	Translate high-level language based programs to assembly-based presentation and vice versa, interpret assembly code to understand or exploit the operation of programs.	<i>L02 (2.7.2), L03 (3.1, 3.2)</i>	<i>Skill - 3</i>

### 4. COURSE LEARNING OUTCOMES

Table 2.

Course outcomes	Descriptions	Level of teaching
<i>G1.1</i>	Describe the functions of components in a computer system	<i>I</i>
<i>G1.2</i>	Describe data presentation in a computer system	<i>I, T</i>
<i>G2.1</i>	Describe the functions of assembly instruction in the operation of programs	<i>I, T</i>
<i>G3.1</i>	Translate high-level language based programs to assembly-based presentation and vice versa	<i>I, T, U</i>
<i>G3.2</i>	Interpret assembly-based program to discover or exploit the operation of program	<i>I, T, U</i>

(*I* – Introduce, *T* – Teach, *U* – Utilize)

### 5. COURSE CONTENT, LESSON PLAN (Nội dung môn học, kế hoạch giảng dạy)

#### a. Theory

Table 3.

Week (1.5 hours)	Contents	Course learning outcomes	Activities	Assessment element
1 (1.5 class hours)	<b>Chapter 1: Introduction to Computer system programming</b> <ul style="list-style-type: none"> <li>- Overview of real-world facts that needs the knowledge of computer system programming.</li> <li>- Overview of course goals, contents</li> <li>- Assessment</li> </ul>	<i>G1.1</i>	<b>Teaching:</b> lecturer gives instructions, question <b>Study in class:</b> exchange related issues, problems. <b>Self-study:</b> do homework	A1, A2
2 (1.5 class hours)	<b>Chapter 2: Data presentation and manipulation</b> <ul style="list-style-type: none"> <li>- Data storing               <ul style="list-style-type: none"> <li>• The base of binary, octal, hexan, decimal</li> <li>• Words, data size</li> <li>• Bitwise operations, shifts</li> </ul> </li> <li>- Integer presentation               <ul style="list-style-type: none"> <li>• Byte ordering: little/big endian</li> <li>• Signed and unsigned integer</li> <li>• Expand and contract numbers</li> </ul> </li> <li>- Arithmetic operations               <ul style="list-style-type: none"> <li>• Unsigned addition</li> <li>• Multiplication, Division</li> </ul> </li> <li>- Floating point               <ul style="list-style-type: none"> <li>• IEEE Floating point 754</li> <li>• Floating point arithmetic operations</li> </ul> </li> </ul>	<i>G1.2</i>	<b>Teaching:</b> lecturer gives instructions, question, demo <b>Study in class:</b> exchange related issues, problems. <b>Self-study:</b> do homework	A1, A2
3, 4 (3 class hours)	<b>Chapter 3.1: Basic to computer-level programming – Assembly</b> <ul style="list-style-type: none"> <li>- Introduction to IA32 and x86_64</li> <li>- Fundamental concepts in computer system programming: registers, memory,...</li> <li>- Assembly-based operations:               <ul style="list-style-type: none"> <li>• Instructions</li> <li>• Operand types: registers, memory, immediates</li> <li>• Data moving, memory addressing</li> </ul> </li> </ul>	<i>G1.1, G1.2, G2.1, G3.1</i>	<b>Teaching:</b> lecturer gives instructions, question, demo, practice exercises with detailed instructions or quizzes <b>Study in class:</b> exchange related issues, problems, do in-class exercises/quizzes	A1, A2

	<ul style="list-style-type: none"> <li>• Two operand and one operand arithmetic instructions</li> <li>- Exercises in basic computer system programming: understanding assembly instructions, operands...</li> </ul>		<b>Self-study:</b> do homework	
5, 6 (3 class hours)	<p><b>Chapter 3.2: Machine-level control</b></p> <ul style="list-style-type: none"> <li>- Condition codes</li> <li>- Application of condition code in controlling program flows: <ul style="list-style-type: none"> <li>• Conditional branches, jump</li> <li>• Loops</li> <li>• Switch</li> </ul> </li> <li>- Exercises in machine-level control: understand assembly presentation of if/else, loops, switch statements...</li> </ul>	<i>G1.1, G2.1, G3.1</i>	<p><b>Teaching:</b> lecturer gives instructions, question, demo, practice exercises with detailed instructions or quizzes</p> <p><b>Study in class:</b> exchange related issues, problems, do in-class exercises</p> <p><b>Self-study:</b> do homework</p>	A1, A2
7 (1.5 class hours)	<p><b>Chapter 3.3: Procedure</b></p> <ul style="list-style-type: none"> <li>- Fundamental concepts in procedure calling in IA32: stack, return address,...</li> <li>- Procedure operations: <ul style="list-style-type: none"> <li>• Control flow from caller to callee and vice versa</li> <li>• Data passing: arguments, return values...</li> <li>• Recursive procedure example</li> </ul> </li> </ul> <p><b>Mid-term examination preparation</b></p>	<i>G1.1, G2.1, G3.1</i>	<p><b>Teaching:</b> lecturer gives instructions, question, demo, practice exercises with detailed instructions or quizzes</p> <p><b>Study in class:</b> exchange related issues, problems, do in-class exercises</p> <p><b>Self-study:</b> do homework, prepare for mid-term exam</p>	A2, A3
8 (1.5 class hours)	<p><b>Chapter 3.3: Procedure (cont)</b></p> <ul style="list-style-type: none"> <li>- Procedure calling in x86_64 compared to IA32.</li> <li>- Exercises in procedure calling.</li> <li>- Introduction to reverse engineering.</li> </ul>	<i>G1.1, G2.1, G3.1, G3.2</i>	<p><b>Teaching:</b> lecturer gives instructions, question, demo, practice exercises with detailed instructions or quizzes</p> <p><b>Study in class:</b> exchange related</p>	A2, A3

			issues, problems, do in-class exercises <b>Self-study:</b> do homework	
9 (1.5 class hours)	<b>Chapter 3.4: Advanced data presentation</b> <ul style="list-style-type: none"> <li>- Array: one-dimensional, nested, multi-level arrays</li> <li>- Structure: allocation, access and alignment in struct</li> <li>- Union</li> <li>- Exercises</li> </ul>	<i>G1.1, G2.1, G3.1</i>	<b>Teaching:</b> lecturer gives instructions, question, demo, practice exercises with detailed instructions or quizzes <b>Study in class:</b> exchange related issues, problems, do in-class exercises <b>Self-study:</b> do homework	A2, A3
10, 11 (3 class hours)	<b>Chapter 3.5: Introduction to buffer overflow</b> <ul style="list-style-type: none"> <li>- Fundamental knowledge of buffer overflow: <ul style="list-style-type: none"> <li>• Vulnerability</li> <li>• Protection</li> </ul> </li> <li>- Exercise of exploiting buffer overflow vulnerability in executable files.</li> </ul>	<i>G2.1, G3.1, G3.3, G3.2</i>	<b>Teaching:</b> lecturer gives instructions, question, demo, in-class buffer overflow exploiting exercise <b>Study in class:</b> exchange related issues, problems, do in-class exercises <b>Self-study:</b> do homework	A2, A3
12 (1.5 class hours)	<b>Chapter 4: Linking</b> <ul style="list-style-type: none"> <li>- Static linking</li> <li>- Object file and ELF format</li> <li>- Symbol and Symbol table</li> <li>- Linker operations: <ul style="list-style-type: none"> <li>• Symbol resolution</li> <li>• Relocation</li> </ul> </li> <li>- Dynamic Linking with Shared Object files</li> </ul>	<i>G1.1, G2.1</i>	<b>Teaching:</b> lecturer gives instructions, question, demo <b>Study in class:</b> exchange related issues, problems <b>Self-study:</b> do homework	A3
13 (1.5 class hours)	<b>Chapter 5: Memory hierarchy and Cache</b> <ul style="list-style-type: none"> <li>- Memory hierarchy</li> <li>- Cache memory</li> </ul>	<i>G1.1, G2.1</i>	<b>Teaching:</b> lecturer gives instructions, question, demo	A3

			<b>Study in class:</b> exchange related issues, problems <b>Self-study:</b> do homework	
14&15 (1.5 class hours)	<b>Course review and final exam preparation</b>	<i>G1-G3</i>	<b>Teaching:</b> lecturer gives instructions, course review, quizzes... <b>Study in class:</b> exchange related issues, problems, do in-class quizzes <b>Self-study:</b> prepare final examination	A3

**b. Lab**

Table 4.

Session (4.5 hours)	Contents	Course learning outcomes	Activities	Assessment element
1	Lab 1: Data Lab	G1.2	<b>Teaching:</b> lecturer describes the objective of the lab and gives instructions for students. <b>Study in class:</b> Students follow the instruction of the lab, report in-class requirements <b>Self-study:</b> Students read the instruction and prepare the lab at home.	A2
2	Lab 2: Basic assembly programming	G2.1	<b>Teaching:</b> lecturer describes the objective of the lab and gives instructions for students. <b>Study in class:</b> Students follow the instruction of the lab, report in-class requirements. <b>Self-study:</b> Students read the instruction and prepare the lab at home.	A2
3	Lab 3: Basic reverse engineering	G2.1, G3.1	<b>Teaching:</b> lecturer describes the objective of the lab and gives instructions for students. <b>Study in class:</b> Students follow the instruction of the lab, report in-class requirements.	A2

			<b>Self-study:</b> Students read the instruction and prepare the lab at home.	
4	Lab 4: Bomb Lab (advanced reverse engineering)	G2.1, G3.1	<b>Teaching:</b> lecturer describes the objective of the lab and gives instructions for students. <b>Study in class:</b> Students follow the instruction of the lab, report in-class requirements. <b>Self-study:</b> Students read the instruction and prepare the lab at home.	A2
5	Lab 5: Bufferbomb Lab (basic buffer overflow)	G2.1, G3.1, G3.2	<b>Teaching:</b> lecturer describes the objective of the lab and gives instructions for students. <b>Study in class:</b> Students follow the instruction of the lab, report in-class requirements. <b>Self-study:</b> Students read the instruction and prepare the lab at home.	A2
6	Lab 6: Bufferbomb Lab (advanced buffer overflow)	G2.1, G3.1, G3.2	<b>Teaching:</b> lecturer describes the objective of the lab and gives instructions for students . <b>Study in class:</b> Students follow the instruction of the lab, report in-class requirements. <b>Self-study:</b> Students read the instruction and prepare the lab at home.	A2

## 6. COURSE ASSESSMENT

Table 5.

Assessment element	Course learning outcomes	Percentage (%)
A1. Mid-term exam	G1.1, G1.2, G2.1, G3.1	30%
A2. Lab	G1.1, G1.2, G2.1, G3.1, G3.2	20%
A3. Final exam	G1.1, G1.2, G2.1, G3.1, G3.2	50%

### a. Assessment A1

The mid-term exam includes (but is not limited to)

- Multiple choice questions: 10 - 12 questions for G1.1, G2.1
- Written: 2-3 questions for G2.1, G3.1.

### b. Assessment A2

The practice test score is the average of 6 practice sessions, each practice has a marking guide.

**c. Assessment A3**

The final exam includes (but is not limited to)

- Multiple choice questions: 10 - 12 questions for G1.1, G1.2, G2.1
- Written: 2-3 questions for G2.1, G3.1, G3.2.

**7. COURSE REQUIREMENTS AND EXPECTATIONS**

- Laboratory: labs can be done in group in laboratories. Lecturer describes the objective of the lab and gives instructions for students. Students must fulfill all lecturer's requirements. Late submission is not accepted or punished according to the reason.
- Class attendance: Students are checked their attendance in class by in-class assignment. Failing to show up by the time of checking is considered to be absent.
- Students need to read slide before the class.
- Mid-term and Final examination: Students that fail to show up on the examination day without acceptable reasons will get 0.

**8. COURSE MATERIALS**

1. Bryant, R. E. & O'Hallaron, D. R. (2015). *A Programmer's Perspective (3<sup>rd</sup> edition)*. Addison-Wesley Publishing Company.
2. Eagle, C. (2008). *The IDA Pro Book: The Unofficial Guide to the World's Most Popular Disassembler*. No Starch Press.
3. Eilam, E. (2011). *Reversing: Secrets of Reverse Engineering*. Wiley.

**9. SOFTWARE, TOOLS**

1. C compiler: GCC/Online compiler (Godbolt)
2. Linux virtual machine.
3. Programming IDE for C/C++.
4. Disassembler: IDA Pro, GDB.

**Date: Month, Date, Year**

**Faculty Head**

**Instructor**