

COURSE OUTLINE CE103 – MICROPROCESSOR AND MICROPROCESSOR

1. GENERAL INFORMATION

Subject Name (Vietnamese):	Vi xử lý & vi điều khiển			
Subject Name (English):	Microprocessors and Microcontrollers			
Course code:	CE103			
Belong to the knowledge	General course □; Basic IT course □;			
block:	Junior CE core course \square ; Senior CE core course \square ; Graduating course \square			
Faculty, Department in	Computer Engineering Faculty			
charge:	Department of Embedded Systems and Robots			
Lecturer:	Trần Ngọc Đức			
	Email: <u>ductn@uit.edu.vn</u>			
Number of credit:	4			
Theory:	3			
Practice:	1			
Self learning:	0			
Prerequisite:				
Previous course:	Computer architecture			

2. COURSE DESCRIPTION

(State Positions of subjects in the curriculum, purpose and main content of the subject; about 3 to 5 lines)

Introducing the knowledge of the 8086 microprocessor's concept, architecture and operating principle and methods for controlling data in and out of microprocessors. At the same time, introduce other modern microprocessors currently in use.

The course also provides microcontroller knowledge of the 805 1 family and provides programming methods, providing examples of microcontroller-based applications in the 8051 life. Also introduced are some other microcontroller families currently used such as AVR, PIC, ARM...

3. COURSE GOALS

After completing this course, students can: Table 1.

Symbo l	Course objectives [1]	Output standard in curriculum [2]
Gl	Having Problem-solving skills	3.2
<i>G2</i>	Having skills to learn and research to solve scientific problems	4.1
G3	System thinking skills and learning ability in the IT industry	5
<i>G4</i>	Having professional ethics	6
<i>G5</i>	Reading professional documents in foreign languages	9.2

4. COURSE LEARNING OUTCOMES

(Course learning outcome (CLO) corresponds to the course objectives in Section 3. The CLO are coded G1 to Gn. There should be no more than 10 CLO.)

Course learning outcomes [1]	Description of Course learning outcomes (Specific Goals) [2]	Level of teaching [3]
G1 (3.2.1)	Applying skills to solve problems in the field of designing embedded systems from simple to complex	ITU
G2 (4.1.1)	Applying skills to learn and research to solve scientific problems	ITU
G3 (5.2)	Applying learning to develope in the Computer Engineering industry	IT
G4 (6.1.1)	Presenting and applying professional ethical requirements	ITU
G5 (9.2.2)	Apply reading and understanding professional documents in foreign languages	ITU

Table 2.

5. COURSE CONTENT, LESSON PLAN

(List the content of teaching theory and practice, showing the correlation with CLO)

a. Theory

Table 3.

	able 3.			
Less on (2.25 hour each) [1]	Content [2]	CLO[3]	Teaching and learning activities [4]	Evaluati on compone nt [5]
Less on 1	Chapter 1. Overview 1.1 General introduction 1.2 Development History 1.3 Introduction of processors 1.4 Introduction of microcontrollers 1.5 The relevant knowledge needs reviewing	<i>G1, G2,</i> <i>G4</i>	Students prepare at home: Reading the slide week 1 provided by the teacher Teachers: Popularize class rules, introduce subject syllabus, provide learning materials for students. Teaching about the introduction of microprocessors, microcontrollers Students in class: Listen to the lecturer's lecture, compare it with the previous knowledge and ask the teacher questions, discuss microprocessor and microcontroller issues. Answer the questions at the end of the class Students study at home: Reviewing the lesson and answer the questions discussed at the end of the week 1 slide in the notebook	A1, A2
Less on 2	Chapter 2. 8086 processor 2.1 Microprocessor architecture 2.2 Overview of 8086 processor 2.3 Internal structure of 8086 microprocessor	<i>G1, G2,</i> <i>G5</i>	Students prepare at home: Reading the slide week 2 provided by teachers Lecturer: Review knowledge of chapter 1, reiterate the embedded system knowledge chain. Teaching about what it processors o verview 8086 Students in class: Listening to lectures by lecturers, compare them with previous knowledge and ask teachers questions, discuss general issues about 8086 microprocessors. Answering the questions at the end of the class Students learn at home: Reviewing lesson and answer the discussion questions at the end of the slide week 2 into the notebook.	A1, A2
Less on 3	Chapter 2. 8086 processor (continued) 2.4 8086 processor hardware architecture	G1, G2, G5	Studentsprepareathome:Readthe week slide 3 provided by teachersLecturer:Reviewchapter2,recallingthe microprocessorknowledgechain.Teachingonthecontents of the	<i>A1, A2</i>

	2.5 Describe the function of processor pins.2.6 Addressing modes		hardware architecture, the function of the pins and the addressing mode of 8086. Students in class: Listen to lectures by lecturers, compare them with previous knowledge and ask teachers questions, discuss issues about 8086. Answering the questions at the end of the class. Students study at home: Reviewing the lesson and answer the questions discussed at the bottom of the slide week 3 in the notebook.	
Less on 4	Chapter 2 . 8086 processor (continued) 2 . 7 Assembly programming for 8086 2.8 Assembly script 2.9 Programming steps 2:10 The general structure of the program 2.11 Basic control structures	<i>G1, G2,</i> <i>G5</i>	Students prepare at home: Read the week slide 4 provided by teachers. Lecturer: Review knowledge of chapter 3, reiterate the microprocessor knowledge chain. Teaching of the contents of 8086 Assembly Students in class: Listen to lectures by lecturers, compare them with previous knowledge and ask teachers questions, discuss issues about 8086 Assembly. Answering the questions and do the end of the lesson Students studying at home: Reviewing the lesson and answer the discussion questions and do the exercise at the end of the week 4 slide in the notebook.	A1, A2
Less on 5	Chapter 2. 8086 processor (continued) 2.12 Communication with memory 2.13 Communication with memory (RAM, ROM) 2.14 Decryption of the memory address	<i>G1, G2,</i> <i>G5</i>	Students prepare in advance: Reading slide slide of week 5 provided by teachers. Referring to the knowledge of memory communication. Lecturer: Review knowledge about Assembly programming in the previous day, check students assignments, reiterate the microprocessor knowledge chain. Lecture on the contents of address decoding and RAM, ROM communication. Students in class: Listening to the lecturer's lecture, compare it with the previous knowledge and ask the teacher questions, discuss memory communication issues. Answering the questions and do exercises in the end of the lesson Students study at home: Reviewing the lesson, answering discussion questions and do homework at the end of the slide of week 5 in the notebook.	A1, A2
Less on 6	Chapter 2 . 8086 processor (continued) 2.15 Communication with peripherals 2.15.1 Communication with peripherals	G1, G2, G5	Students prepare at home: Read the slide week 6 provided by the teacher. Referring to the knowledge of communication and the peripheral expansion of 8086	A1, A2

	2.15.2 Communicate		Lecturer: Review knowledge of memory	
	with 8255 2.15.3 Communicate with 8251		communication, examine students' assignments, and repeat the series of microprocessor knowledge. Teaching on issues of communication peripherals and	
			peripheral expansion in 8086 Students in class: Listening to lectures by lecturers, comparing with previously knowledge and asking teachers questions,	
			discussion of issues of peripheral communication. Answering the questions and	
			do the end-of-class exercises Students studying at home: Reviewing the lesson, answering the questions discussed at the bottom of the week 6 slide in the notebook.	
Less on 7	Chapter 2. 8086 processor (continued) 2.15 Communicate with peripheral devices 2.15.4 Communication with Timer 8253 2.15.5 Communication with Interrupt 8259 2.15.6 Communicate	G1, G2, G5	Students prepare at home: Reading the slide week 7 provided by the teacher. Referring to knowledge about Timer communication and co-processing Lecturer: Reviewing knowledge of sensor, reiterate about microprocessor knowledge chain. Teaching about Timer and co- processing communication. Students in class: Listening to lectures by	A1, A2
	with 8087 co-processor		lecturers, compare with previously knowledge and ask teachers questions, discuss Timer 8259 issues and co-handle 8087 and related knowledge. Answering the questions and do the end-of-class exercises Students study at home: Reviewing lessons, answer discussion questions and do homework at the end of the week 7 slide into the notebook	
Less on 8	Mid-semester review	G2, G3, G5	Students prepare at home: Read the slide week 8 provided by the teacher, review all chapters 1 and 2 and the lecture slides from week 1 to week 7 and review the previous exercises .	A1, A2
			Teachers: Give students a 30-minute test to test students' knowledge. Reviewing knowledge of chapters 1 and 2 and repeat the exercises done, paying special attention to the exercises and knowledge that students have not yet learned on the test. Answering students' questions	
			Students studying in class: Take tests, monitor and listen to lecturers, record review contents and necessary exercises	

			Students studying at home: Reviewing knowledge and exercises	
Less on 9	Chapter 3. 8051 microcontroller 3.1 General introduction 3.2 Hardware architecture	<i>G1, G2, G5</i>	.Students prepare at home: Reading the slide week 9 is provided by teachers.Referring to the basics of 8051 microcontroller.Lecturer: Review memory organization knowledge, reiterate the microcontroller knowledge chain. Lectured on the basics of 8051.Students studying in class: Listen to lectures by lecturers, compare with previous knowledge and ask questions of lecturers, discuss basic overview issues about 8051 and related knowledge. Answer the questions and do the end-of-class exercises Students study at home: Review the lesson, answer the discussion questions and do the exercises at the end of the week 9 slide in the notebook.	A1, A4
Less on 10	Chapter 3. 8051 microcontroller (continued) 3.3 8051 microcontroller architecture 3.4 Organization of memory 3.5 Addressing modes	<i>G1, G2, G5</i>	Notebook.Students prepare at home: Reading the slide week 10 provided by the teacher. Referring to the knowledge of 8051 architecture and memory organization. Lecturer: Reviewing general knowledge of microcontrollers 8051, reiterated the microcontroller knowledge chain. Teachin g about 8051 architecture issues and organize its memory.Students in class: Listen to lectures by lecturers, compare with previously read knowledge and ask teachers questions, discuss 8051 architecture issues and organize memory and related knowledge. Answer the questions and do the end-of- class exercisesStudents study at home: Review the lesson, answer the discussion questions and do the exercises at the end of the week 10 slide in the notebook.	A1, A4
Less on 11	Chapter 3. 8051 microcontroller (continued) 3.6 Programming Assembly language on 8051 3.7 Assembly programming on 8051 3.8 General structure for the program	<i>G1, G2,</i> <i>G5</i>	Week 10 stude in the notebook.Students prepared at home: Read the slideweek 11 provided by the teacher. Refer tomore knowledge about programming on 8051Assembly.Lecturer: Review knowledge of the 8051structure and organize memory, recalling themicrocontroller knowledge chain. Lectureon Assembly on 8051 programmingStudents in class: Listen to lectures bylecturers, compare them with previous	A1, A4

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Students study at home: Review the lesson,					
answer the discussion questions and do the					
exercise at the end of the week 13 slide in the				-	
notebook.					
Less Chapter 4: Introduction <i>G1, G2,</i> Students prepared at home: Read the slides of <i>A1, A4</i>	Less	Chapter 4: Introduction	<i>G1. G2.</i>	Students prepared at home: Read the slides of	A1. A4
$\begin{array}{c c} \hline \textbf{on} & \textbf{of modern} \\ \hline \textbf{on} & \textbf{on} \\ \hline $,
$\begin{array}{c} \text{in controllers} \\ 14 \end{array} \begin{array}{c} \text{in controllers} \\ \text{G5} \end{array} \begin{array}{c} \text{weak } 14 \text{ tradener supply. Refer to the basies of } \\ \text{AVR, PIC and ARM.} \end{array}$					
	ΤT	4.1 AVR Microcontroller		Lecturer: Review knowledge of interrupt	
		4.2 PIC microcontrollers		-	
			1		
		4.3 ARM microcontrollers		the microcontroller	

Less on 15	Project report and end of semester review	G2, G3, G5	knowledge chain. Teaching about Modern microcontrollers like AVR, PIC and ARM Students in class: Listen to lectures by lecturers, compare them with previous knowledge and ask teachers questions, discuss basic issues of AVR, PIC and ARM and related knowledge. Answer the questions and do the end-of-class exercises Students study at home: Review the lesson, answer the discussion questions and do the exercises at the end of the week 14 slide in the notebook. Students prepare in advance: Read the slide of week 15 provided by the teacher, review all chapters 3,4 and slides from week 9 to week 14 and review the previous exercises. Teachers: Give students a 30-minute test to test students' knowledge. Review knowledge of chapters 3 and 4 and repeat the exercises done, paying special attention to the exercises	A1, A4
			and knowledge that students have not yet learned on the test. Answer students' questions Students studying in class: Take tests, monitor and listen to lecturers, record review contents and necessary exercises Students studying at home: Review the knowledge and exercises (in the review exercise file, and the exercises in the slide of week 15)	

b. Practice

Lesson (3.75 hours each)	Content	CL O	Teaching and learning activities	Evaluation component
Lesson 1	Exercise 1: Familiarize with programming and compiling Assembly for 8086 on Emu8086	G1, G2, G5	Students prepare at home: Read Exercise # 1 in the microprocessor practice guide and prepare the software to practice according to the instructions. Trainers: Disseminate practice rules, guide students step by step to install the program and instruct students how to perform basic commands in the practice to get used to Assembly and Emu8086, how to write report and submit practice reports Students learn at the same time: Install the program, follow the instructions of the instructor, perform practical exercises, write and submit the practice report according to regulations.	<i>A</i> 3

			Students studying at home: Do additional exercises and submit assignments as prescribed	
Lesson 2	Exercise 2: Realization of calculation on 8086	G1, G2, G5	Students prepare at home: Read exercise # 2 in the microprocessor practice guide and learn about the calculations made on 8086 Lecturer: On guidance to students using assembly programming on 8086 calculations. Students learn at the same time: Listen and follow the instructor's steps, perform practical exercises on calculating 8086 Students studying at home: Do additional exercises and submit assignments as prescribed	<i>A</i> 3
Lesson 3	Exercise 3: Communication 8086 with 8255	G1, G2, G5	Students prepared at home: Read the exercise #3 in practices documentation. Understand processor and investigate carefully Assembly programming in interfacing between 8086 to 8255. Lecturer: Teach students how to write programs communication 8086 and 8255 Classroom students: Listen and follow the instructor's step, perform practical exercises on reading communication 8255 Students studying at home: Do additional exercises and submit assignments as prescribed	<i>A</i> 3
Lesson 4	Exercise 4: LED Display, Timer, Interrupt for 8051	G1, G2, G5	Students prepare at home: Read exercise # 4 in the microcontroller practice guide and learn more about it programming Timer LED and Interrupt for 8051 Lecturer: Remind the principles of peripheral control, LED on/off programming, Timer control and Interrupt processing. Students in class: Listen and follow the steps of the faculty's trainers, perform the exercises on the microcontroller, write and submit reports as prescribed Students studying at home: Do additional exercises and submit assignments as prescribed	<i>A</i> 3
Lesson 5	Exercise 5: Design clock pulse, control 7- segment LED using Timer interrupt for 8051	G1, G2, G5	Students prepare at home: Read exercise # 5 in the microcontroller practice guide and learn the knowledge to design Clock, control 7-segment LED. Teachers: Remind me of the knowledge of the clock design and interrupt use in 8051 to control 7-segment LED. Guide students to write successful interrupt handling.	<i>A</i> 3

			Students study in class: Listen and follow the instructor's steps, perform practical exercises on using interrupt for 7-segment LED control. Students studying at home: Do additional exercises and submit assignments as prescribed	
Lesson 6	<i>Exercise 6:</i> <i>Sub-project reports and</i> <i>practice tests</i>	G1, G2, G5	Students prepare at home: Read exercise # 6 in the microcontroller practice guide. Instructors: Remind knowledge of using hardware and software to fully solve a project using 8051, with interrupt and peripheral control. Students in class: Listen and follow the instructor's step, practice the exercises subproject. Students studying at home: Do additional exercises and submit assignments as prescribed	A3

6. COURSE ASSESSMENT

Table 5.

Evaluation component [1]	Course learning outcomes [2]	Rate (%) [3]
A1. Learning Process (Test on class, assignments)	<i>G1, G2, G5</i>	15%
A2. Mid-term Semester	G1, G2, G3, G4, G5	15%
A3. Practice	<i>G1, G2, G5</i>	20%
A4. Final Semester Test	G1, G2, G3, G4, G5	50%

[1]: Evaluation components of the subject. [2]: List the corresponding course learning outcome evaluated by the evaluation component. [3]: Scores of assessments on the total subject score.

Rubric of each component evaluated in Table 5

Examination in class	Excellence (8- 10đ)	Good (6-7đ)	Average(5đ)	Weak(3-4đ)	Bad(0-3đ)
Ask for old lessons, new lessons and do class work (test self-studying ability of college students in G2)	Volunteer to answer exercise 4 - 5 times	Volunteer to answer or board the exercise 3 times	Volunteer to answer or go to the board to do the exercise 2 times	Volunteer to answer or board the exercise 1 time	Volunteer to answer or board the exercise 0 times
<i>Attendance is completed</i>	Attend full attendance at 100% attendance sessions	Attend school 75 % attendance sessions	Attend 50% of attendance sessions	Attend 25% of attendance sessions	Not going to school
15-minute test	Excellence (8- 10đ)	Good (6-7đ)	Average(5đ)	Weak(3-4đ)	Bad(0-3đ)
Test No. 1 on programming Assembly on 8086 (Check knowledge of embedded system G1, G5)	Fully present the 8086 instruction set and solve assembly language exercises on 8086	Present the 8086 instruction set correctly and solve part of the assembly language exercises on 8086	Present the correct 8086 instruction set	Partial presentation o f 8086 instruction set	Misrepresent the script and not solve the assembly programming language on 8086
Test No. 2 on programming on 8051 Assembly (Check knowledge of operating system G1, G5)	Fully present the 8051 script and solve assembly language exercises on 8086	Present the 8051 script correctly and solve part of the assembly language exercises on 8086	Present the correct 8051 script	Present part of the 8051 script correctly	Misrepresent the script and not solve assembly language exercises on 8051

a. Rubric of A1 rating components

b. Rubric of A2 rating component

Quiz section	Excellence (8-10đ)	Good (6-7đ)	Average(5đ)	Weak(3-4đ)	Bad(0-3đ)
8086 microprocess or definitions, concepts and principles	Correctly identify 80 to 100% of definitions, concepts and knowledge related to the 8086- microprocess or overview	Identify exactly 60 to 80% of the definitions, concepts and knowledge related to the 8086- microprocess or overview	Correctly identify 50 to 60% of the definitions, concepts and knowledge related to the 8086- microprocessoverv iew	Correctly identify 30 to 50% of the definitions, concepts and knowledge related to the 8086- microprocess or overview	Correctly identify under 30% of definitions, concepts and knowledge related to the overview of 8086 microprocessors
8086 processor, script, assembly use and memory communicati on problem	Correctly identify 80 to 100% of definitions, concepts and knowledge related to using Assembly and memory communicati on issues	Identify exactly 60 to 80% of definitions, concepts and knowledge related to using Assembly and memory communicati on issues	Correctly identify 50 to 60% of definitions, concepts and knowledge related to using Assembly and memory communication issues	Correctly identify 30 to 50% of definitions, concepts and knowledge related to using Assembly and memory communicati on issues	Identify well below 30% of definitions, concepts and knowledge related to using Assembly and memory communication issues
8086 processor, peripheral programmin g 8255, 8251	Correctly identify 80 to 100% of definitions, concepts and knowledge related to peripheral programmin g 8255, 8251	Identify exactly 60 to 80% of definitions, concepts and knowledge related to peripheral programmin g 8255, 8251	Correctly identify 50 to 60% of definitions, concepts and knowledge related to peripheral programming 8255, 8251	Correctly identify 30 to 50% of definitions, concepts and knowledge related to peripheral programmin g 8255, 8251	Identify well below 30% of definitions, concepts and knowledge related to peripheral programming 8255, 8251

8086 processor, peripheral programmin g, Timer 8254, Interrupt, and 8087 co- processor	Identify exactly 80 to 100% of definitions, concepts and knowledge related to peripheral programmin g, Timer 8254, Interment	Identify exactly 60 to 80% of definitions, concepts and knowledge related to peripheral programmin g, Timer 8254, Interment	Correctly identify 50 to 60% of definitions, concepts and knowledge related to peripheral programming, Timer 8254, Interrupt, and co- processor 8087	Identify exactly 30 to 50% of definitions, concepts and knowledge related Peripheral programmin g, Timer 8254, Interment	Correctly identify under 30% of definitions, concepts and knowledge related to peripheral programming, Timer 8254, Interrupt, and co- processor 8087
	Interrupt, and co- processor 8087	Interrupt, and co- processor 8087		Interrupt, and 8087 co- processor	
The essay section	Excellence (8-10đ)	Good (6-7đ)	Average(5đ)	Weak(3-4đ)	Bad(0-3đ)
Design of memory communicati on system and address decoding.	Properly and completely present the memory communicati on system design and address decoding an d properly drawing status tables	Proper and complete presentation of memory communicati on system design and address decoding	Correct presentation of memory communication system design and address decoding	Correct presentation of memory communicati on system design and address and mixed decoding but there are errors in draw state tables	Wrong presentation on memory communication system design and address decoding
Using 8086 Assembly language in peripheral communicati on Interrupt, Timer	Correct and complete presentation of using 8086 Assembly Language in peripheral communicati on Interrupt and Timer	Correct and complete presentation of 8086 Assembly language using in Interrupt or Timer peripheral communicati on and incomplete in the remaining peripheral	Correct description of using 8086 Assembly language in peripheral communication Interrupt and Timer	Correctly presented using the 8086 Assembly language in peripheral Interrupt or Timer	Misrepresented the use of assembly language 8086 in communication peripheral and Timer Interrupt

c. Rubric of A3 evaluation components

Report the exercises	Excellence (8- 10đ)	Good (6- 7đ)	Average(5đ)	Weak(3- 4đ)	Bad(0-3đ)
Report programming and using Assembly for 8086 on Emu8086	Complete full command and full implementatio n of all practical exercises	Complete orders and execute 4/7 practical exercises	Complete the orders and execute 2/7 practical exercises	Complete orders or perform 3/7 practical exercises	Do not complete the commands and do not complete the exercises
<i>Report on calculation on</i> 8086	Complete the examples and fully implement practical exercises	Complete the examples and implement 3/4 practical exercises	Complete the examples and perform 2/4 practical exercises	Complete the examples or perform 2/4 practical exercises	Do not complete the examples and do not complete the exercises
<i>Report of 8086</i> <i>communication practice wit</i> <i>h 8255</i>	Complete the examples and fully implement practical exercises	Complete the examples and perform 4/6 practical exercises	Complete the examples and perform 2/6 practical exercises	Complete the examples or perform 3/6 practical exercises	Do not complete the examples and do not complete the exercises
Report application control LED Display, Timer, Interrupt for 8051	Complete the examples and fully implement practical exercises	Complete the examples and implement 3/4 practical exercises	Complete the examples and perform 2/4 practical exercises	Complete the examples or perform 2/4 practical exercises	Do not complete the examples and do not complete the exercises
Report on clock pulse design, control 7 segment LED using Timer interrupt for 8051	Complete the examples and fully implement practical exercises	Complete the examples and perform 4/6 practical exercises	Complete the examples and perform 2/6 practical exercises	Complete the examples or perform 3/6 practical exercises	Do not complete the examples and do not complete the exercises

Sub-project report	Complete the examples and fully implement practical exercises	Complete the examples and implement 3/4 practical exercises	Complete the examples and perform 2/4 practical exercises	Complete the examples or perform 2/4 practical exercises	Do not complete the examples and do not complete the exercises
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a. Rubric of A4 evaluation components

Quiz section	Excellence (8- 10đ)	Good (6-7đ)	Average(5đ)	Weak(3-4đ)	Bad(0-3đ)
8086 microp rocesso r definiti ons, concep ts and princip les	Correctly identi fy 80 to 100% of 8086 processor defini tions, concepts, principles	Identify exactly 60 to 80% of 8086 microprocessor de finitions, concepts , principles	Correctly identify 50 to 60% of 8086 microprocessor de finitions, concepts , principles	Identify exactly 30 to 50% of 8086 microprocessor de finitions, concepts , principles	Correctly ide ntify under 30% of 8086 microprocess or definitions, concepts and principles
8051 Assem bly pro blem, address ing modes	Properly identifying 80 to 100% of the issue programm ing Assembly in 8051, addressing modes	Correctly identify 60 to 80% of the Assembly problems on 8051, addressing modes	Correctly identify 50 to 60% of 8051 Assembly programming pro blems, addressing modes	Correctly identify 30 to 50% of 8051 Assembly programming pro blems, addressing modes	Correctly ide ntify under 30% of progr amming issue s on 8051, addressing modes
Periph eral commu nicatio n proble m in 8051: Timer, UART, Interru pt	Properly identifying 80 to 100% of the thought a princi ple, the communicat ion peripherals in 8051: Timer, UART, Interrupt	Properly identifying 60 to 80% of all the thought a principl e, communication peripherals in 8051: Timer, UART, Interrupt	Properly identifying 50 to 60% of all the thought a principl e, communication peripherals in 8051: Timer, UART, Interrupt	Properly identifying 30 to 50% of all the thought a principl e, communication peripherals in 8051: Timer, UART, Interrupt	Properly identifying 30% of the thought a prin ciple, commu nication peripherals in 8051: Timer, UART, Interrupt
The moder n microc ontroll er	Identify exactly 80 to 100% of modern microcontroller knowledge	Identify exactly 60 to 80% of the knowledge about modern microcontrollers	Correctly identify 50 to 60% of modern microcontroller knowledge	Identify exactly 30 to 50% of modern microcontroller knowledge	Identify less than 30% of knowledge about modern microcontroll ers
The essay section	Excellence (8- 10đ)	Good (6-7đ)	Average(5đ)	Weak(3-4đ)	Bad(0-3đ)

System design 8051 externa 1 memor y and periphe ral commu nicatio n	Present properly and fully designed memory communication and external peripheral	Present 60 to 70% of the correct memory and peripheral peripheral design	Present exactly 50 to 60% of memory and peripheral peripheral design	Present exactly 30 to 50% of memory and peripheral peripheral design	Presents less than 30% of memory and peripheral peripheral design
Use Assem bly 8051 langua ge in periphe ral commu nicatio n Interru pt, Timer	Present properly and fully on using Assembly 8051 language in peripheral communication Interrupt and Timer	Present properly and fully on using Assembly 8051 language in Interrupt or Timer peripheral communication and incomplete in the remaining peripheral	Correct description of using Assembly 8051 language in peripheral communication Interrupt and Timer	Correctly presented using the Assembly 8051 language in peripheral Interrupt or Timer communication	Wrong presentation of using Assembly 8051 language in peripheral communicati on Interrupt and Timer

7. COURSE REQUIREMENTS AND EXPECTATIONS

(State other courses regulation, if any, example: students who do not submit assignments or report on will be consider as not submitting; Students who miss practice 2 session will not be allowed to take a final exam, ...)

- Attendance: according to school regulations.
- Theory class:
 - Students need to print lecture slides and read in advance at home before class.
 - In the class, student aren't allowed to make noise, or sleep-in class, attentive listening and full notes.
 - Proactively answer questions from teachers and volunteer on the worksheet, ask when having questions.
 - Review the lesson at home after each lesson, if student still don't understand what knowledge content you can send an email to ask or ask in the next lesson.
- Practical class:
 - Students must not miss more than 3 lesson in the total number of practice exercise. There will be 0 points for practice.
 - Students who miss any practical lesson without first obtaining permission for acceptable reason will receive a zero for that practice session.
 - Read the instruction manual before each practice session. Pay attention to the instructor for practice. Ask questions when you have questions.

- Focus on doing practice, not surfing the web, reading Facebook, playing games, chatting on the Internet ... However, you can discuss with each other in class about the exercise you are doing.
- When submitting the exercise, students should note that each copy of each copy will get a score of 0.
- Theoretical test: do not cheat, ask other students when taking the exam. If the supervisor finds out, the answer sheet will be marked and depending on the extent of the violation the score may be deducted or given a score of 0 for the test.

8. LEARNING MATERIAL, REFERENCE

(The number of textbooks does not exceed 3 documents, the number of reference documents does not exceed 10 documents, in the process of teaching, lecturers can provide additional reference material other than this category.)

Curriculum

1. Phan Dinh Duy. Lecture + Slide subject of Microprocessor - Microcontroller, University of Information Technology (internal circulation), 2015.

2. Vu Duc Lung, Nguyen Quang Minh and Phan Dinh Duy. Microcontroller syllabus. Publisher of VNU HCMC, 2016, 207 pages.

3. Tong Van On. System design with 8051 family. Phuong Dong Publishing House, 2006, 460 pages.

References

1. Kenneth Ayala. 8051 Microcontroller: Architecture, Programming and Applications Second Edition. Delmar Learning Publishing House, 1996, 367 pages.

2. Walter A. Triebel, Avtar Singh. The 8088 and 8086 Microprocessors: Programming, Interfacing, Software, Hardware, and Applications 4th Edition. Prentice-Hall, 2002, 1019 pages.

3. Douglas v. Hall. Microprocessors and Interfacing: Programming and Hardware. Publisher Glencoe McGraw-Hill, 1991, 624 pages .

9. SOFTWARE OR TOOLS SUPPORTING PRACTICE

- 1. Programming simulation software for 8086 (Emu8086)
- 2. Programming simulation software for 8051 (Protus 8.1)

HCMC, date 01 month 3 year 2019

Head of department / subject

(Sign and write full name)

Lecturer

(Sign and write full name)